2. (Amended) The wireless communication terminal according to claim 1, said operation clock control means including means for reducing said frequency of said operation clock as said strength of said receiving electric field decreases.

3. (Amended) The wireless communication terminal according to claim 1, wherein:

said detecting means includes memory means for storing a measured value of said strength of said receiving electric field; and

said operation clock control means includes means for controlling said frequency of said operation clock based on said measured value stored by said memory means.

4. (Amended) The wireless communication terminal according to claim 1, wherein:

said detecting means includes means for selecting and maintaining a receiving level inferential value from a plurality of previously stored receiving level inferential values, wherein said selected receiving level inferential value corresponds to said detected receiving electric field strength; and

said operation clock control means includes:

a plurality of operation clock generation means that generate operation clocks corresponding to said receiving level inferential values; and

selecting means for selecting one of said plurality of operation clock generation means corresponding to said receiving level inferential value that is maintained by said detecting means.

5. (Amended) The wireless communication terminal according to claim 1, wherein:

said wireless transmitting/receiving means performs transmitting/receiving processing in accordance with a Time Division Multiple Access (TDMA) communication method; and

said operation clock control means controls said frequency of said operation clock in synchronization with a timing of a time division receiving operation of said wireless ~~transmitting/receiving section~~.

6. (Amended) The wireless communication terminal according to said claim 5:

said wireless transmitting/receiving means including means for generating an interrupt signal at a starting time of a receiving slot of said time division receiving operation and an interrupt end signal at an ending time of said receiving slot; and

said operation clock control means including means for controlling said operation clock frequency according to said receiving electric field strength in response to said interrupt signal, and means for ending said control of said operation clock frequency in response to said interrupt end signal.

8. (Amended) In a wireless communication terminal, a method of controlling an operation clock for processing transmitted/received data, said method comprising the steps of:

detecting a strength of a receiving electric field; and

controlling a frequency of said operation clock based on said detected strength of ~~said~~ receiving electric field.

9. (Amended) The method according to claim 8, wherein said controlling step includes a step of controlling said frequency of said operation clock by reducing said frequency of said operation clock as said detected strength of said receiving electric field decreases.

10. (Amended) The method according to claim 8, wherein:

said detecting step includes a step of selecting, out of said plurality of receiving level inferential values, a receiving level inferential value corresponding to said detected strength of said receiving electric field; and

said control step includes a step of selecting, out of said plurality of operation clocks, an operation clock corresponding to said selected receiving level inferential value.

11. (Amended) The method according to claim 8, wherein:

said processing of said transmitted/received data is performed in accordance with a Time Division Mulitple Access (TDMA) communication method; and

said controlling step includes a step of controlling said frequency of said operation clock in synchronization with a timing of a time division receiving operation.

12. (Amended) The method according to claim 11, wherein said controlling step includes a step of starting to control said frequency of said operation clock at a starting time of a receiving slot and ending said control of said frequency of said operation clock at an ending time of said receiving slot.

13. (Amended) The method according to claim 12, wherein said controlling step includes a step of returning said frequency of said operation clock to an original frequency of said operation clock at said ending time of said receiving slot.

14. (New) A wireless communication terminal comprising:

a wireless transceiver that transmits and receives data;

an electric field detector that detects a strength of an electric field received by the wireless transceiver;

an operation clock; and

a control circuit that changes the frequency of the operation clock based on the detected strength of the received electric field.

15. (New) A wireless communication terminal according to claim 14, further comprising a central processing unit (CPU), wherein an operating speed of the CPU changes according to the frequency of the operation clock.

16. (New) A method for controlling a frequency of an operation clock in a wireless communication terminal, the method comprising:

detecting a strength of an electric field received by the wireless communication terminal; and

changing the frequency of the operation clock based on the detected strength of the received electric field.

17. (New) The method according to claim 16, further comprising changing an operating speed of a central processing unit in accordance with the frequency of the operation clock.